



PATENT ABSTRACTS OF JAPAN

(11) Publication number: **63111474 A**(43) Date of publication of application: **16.05.88**

(51) Int. Cl.

G01R 31/02(21) Application number: **61256891**(71) Applicant: **EASTERN:KK**(22) Date of filing: **30.10.86**(72) Inventor: **NAKAMURA YOSHINORI****(54) METHOD AND DEVICE FOR INSPECTING
PATTERN OF PRINTED WIRING BOARD****(57) Abstract**

PURPOSE: To increase the reliability of an inspection result by placing a probe pin and a terminal in a normal contact state when a conductive mat is extracted, and inspecting the breaking and short circuit of a wire in this state.

CONSTITUTION: A printed wiring plate 2 is set by being clamped between a support base 1 and a pin board 4, and the probe pin 5 is made to contact an A terminal. At the same time, the conductive mat 3 mounted on an up/down moving mechanism 7 is brought into contact with a B terminal. In this state, respective probe pin are fed with electricity to inspect the breaking of the pattern of the wiring board 2. Then the mechanism 7 is driven without varying the contact resistance between the pin 5 and A terminal to move the conductive mat 3 away from the B terminal, and the respective pins are fed with electricity in this state to inspect the short circuit of the pattern of the wiring board 2 from whether the pins are conductive or not.

COPYRIGHT: (C)1988,JPO&Japio

